

6th IEEE International Symposium on Smart Electronic Systems

(DEC 14-16, 2020, CHENNAI, INDIA)

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The primary objective of **IEEE-iSES** is to provide a platform for both hardware and software researchers to interact under one umbrella for further development of smart electronic systems. Efficient and secure data sensing, storage, and processing play pivotal roles in current information age. The state-of-the-art smart electronic systems cater to the needs of efficient sensing, storage, and computing. At the same time, efficient algorithms and software used for faster analysis and retrieval of desired information are becoming increasingly important. Big data which are large, complex data sets, are now a part of the Internet world. Storing and processing needs of the enormous amount of structured and unstructured data are getting increasingly challenging. At the same time, Internet of Things (IoT) and cyber-physical systems (CPS) have been evolving with simultaneous development of hardware and software and span across everyday consumer electronics. The performance and efficiency of the present as well as the future generations of computing and information processing systems are largely dependent upon advances in both hardware and software. IEEE-iSES is a sponsored meeting of Technical Committee on VLSI, IEEE-CS that endorses a league of successful meetings including ASAP, ISVLSI, and ARITH, are now presented as “Sister Conferences”.

CONTRIBUTIONS ARE SOUGHT IN THE FOLLOWING AREAS :

- Nanoelectronic VLSI and Sensor Systems (NVS)
- Energy-Efficient, Reliable VLSI Systems (ERS)
- Hardware/Software for Internet of Things and Consumer Electronics (IoT)
- Hardware for Secure Information Processing (SIP)
- Hardware/Software for AI, Robotics, and Automation (AIR)
- Hardware/Software for Vehicular Intelligent Systems (VIS)

IEEE-iSES technical program will include both contributed papers and contributions from invited speakers. IEEE-iSES 2020 will have **keynote addresses, plenary talks, technical sessions, special sessions, expert panels, research demo session (RDS) and a student research forum (SRF)**.

TCVLSI sponsored several student travel and best paper awards available.

PAPER SUBMISSION DEADLINES:

Submission (hard deadline) : August 31, 2020 Sep 14, 2020

Acceptance : Sep 15, 2020 Oct 24, 2020

Final Version: Sep 30, 2020 Nov 8, 2020

IEEE-iSES proceedings will be published by IEEE-CS conference publications services (CPS). Authors should submit their original unpublished work of maximum 6 pages using IEEE-CS double-column conference format-template. Manuscripts in PDF format with author information (optional) should be submitted using the link: <https://edas.info/index.php?c=27327>. Selected papers from IEEE-iSES 2020 program will be invited for submission to a special issue peer-reviewed journal.

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