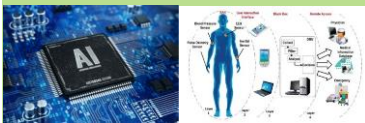
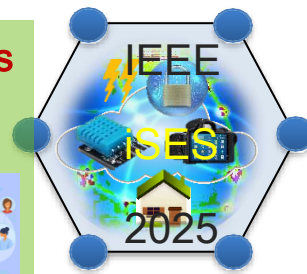


# 11<sup>th</sup> IEEE International Symposium on Smart Electronic Systems

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